



Features:

- Isolated mounting base 3000V~
- Pressure contact technology with increased power cycling capability
- Space and weight saving

Typical Applications

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

V _{DSM} , V _{RSM}	V _{DRM} , V _{RDM}	Type & Outline
900V	800V	MTx26-08-223F3B
1100V	1000V	MTx26-10-223F3B
1300V	1200V	MTx26-12-223F3B
1500V	1400V	MTx26-14-223F3B
1700V	1600V	MTx26-16-223F3B
1900V	1800V	MTx26-18-223F3B

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _J (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Single side cooled, T _c =85°C	125			26	A
I _{T(RMS)}	RMS on-state current		125			41	A
I _{DRM} I _{RDM}	Repetitive peak current	at V _{DRM} at V _{RDM}	125			8	mA
I _{TSM}	Surge on-state current	10ms half sine wave	125			0.5	KA
I ² t	I ² T for fusing coordination	V _R =60%V _{RDM}				1.25	A ² s*10 ³
V _{TO}	Threshold voltage		125			0.85	V
r _T	On-state slop resistance					9.68	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =80A	25			1.69	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =67%V _{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t _r ≤0.5μs Repetitive	125			200	A/μs
I _{GT}	Gate trigger current		25	30		100	mA
V _{GT}	Gate trigger voltage	V _A =12V, I _A =1A		0.8		2.5	V
I _H	Holding current			20		120	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125	0.2			V
R _{th(j-c)}	Thermal resistance Junction to case	Single side cooled per chip				0.950	°C /W
R _{th(c-h)}	Thermal resistance case to heatsink	Single side cooled per chip				0.2	°C /W
V _{iso}	Isolation voltage	50Hz, R.M.S, t=1min, I _{iso} :1mA(MAX)		3000			V
F _m	Thermal connection torque(M5)				4.0		N·m
	Mounting torque(M6)				6.0		N·m
T _{stg}	Stored temperature			-40		125	°C
W _t	Weight				160		g
Outline	223F3B						

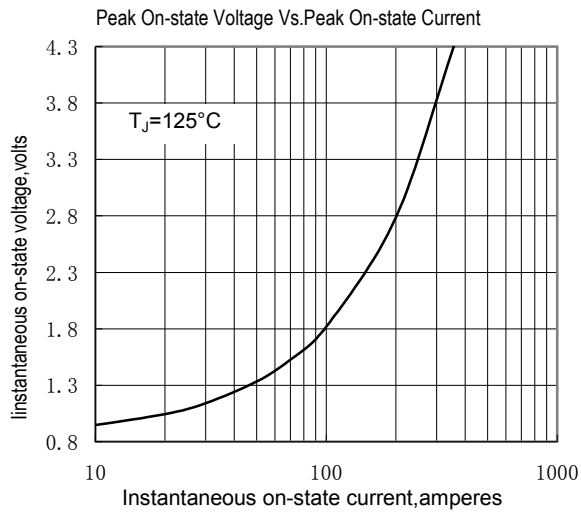


Fig.1

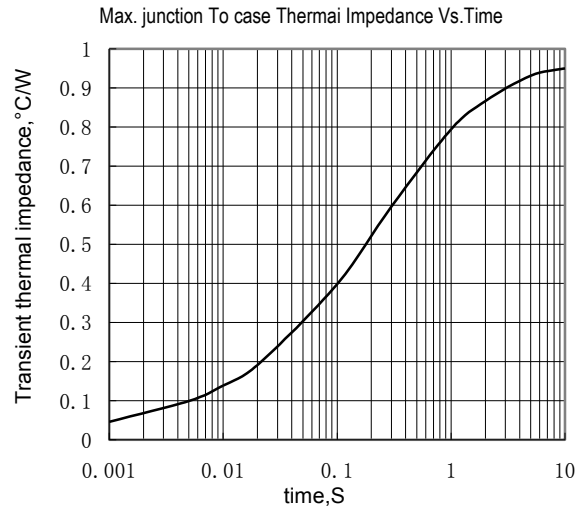


Fig.2

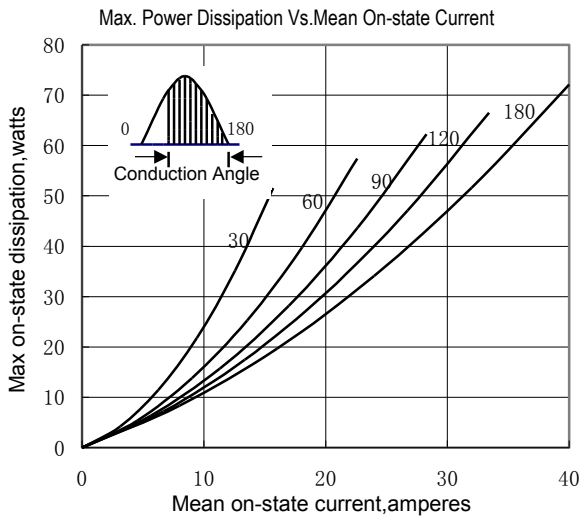


Fig.3

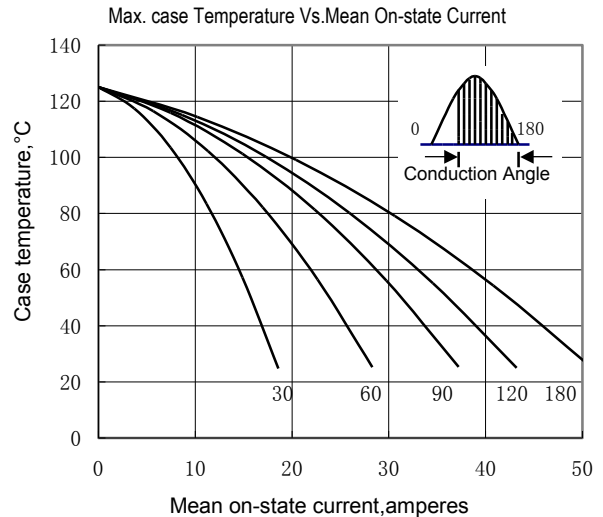


Fig.4

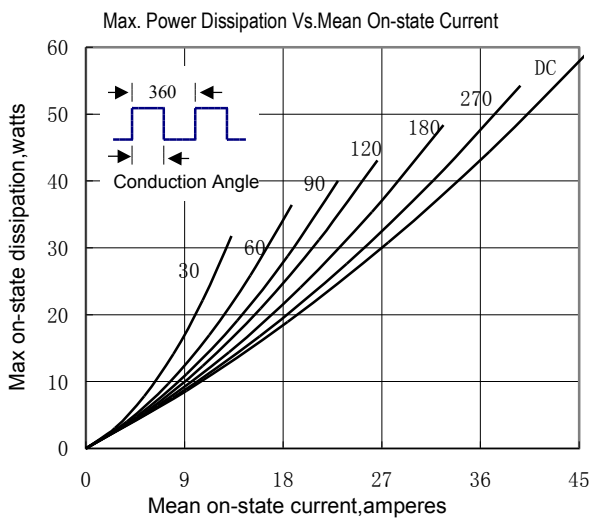


Fig.5

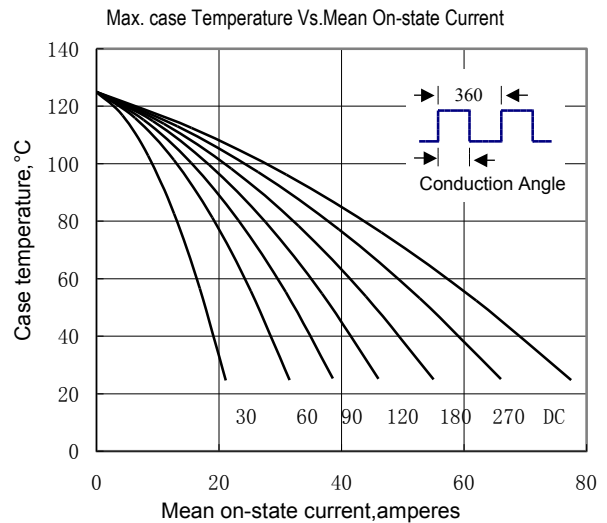


Fig.6

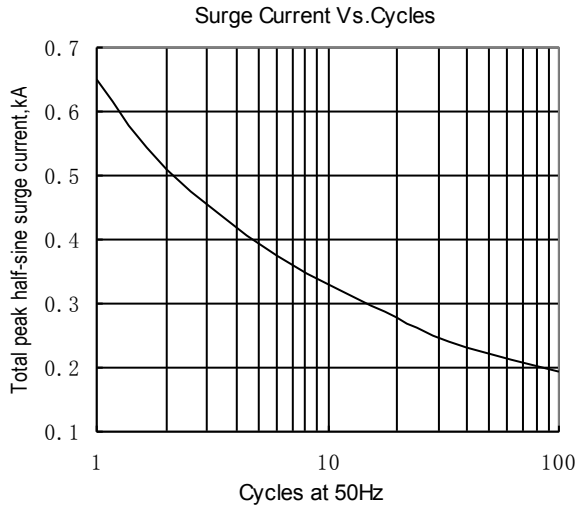


Fig.7

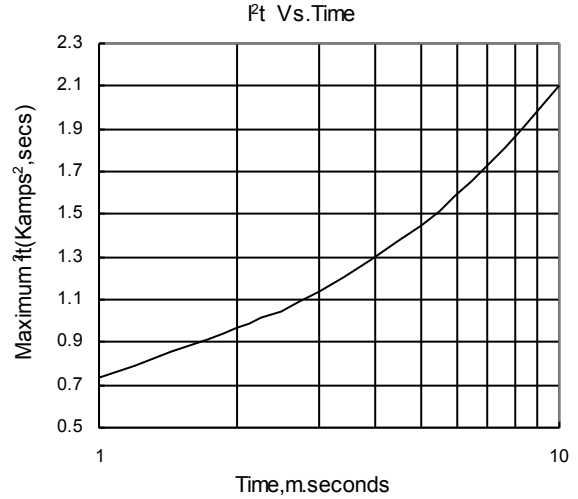


Fig.8

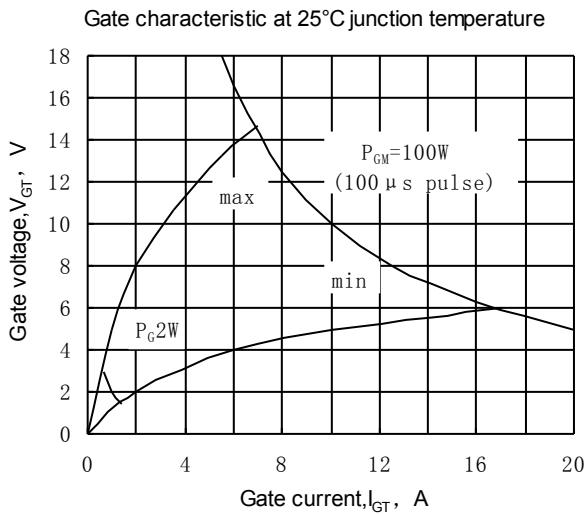


Fig.9

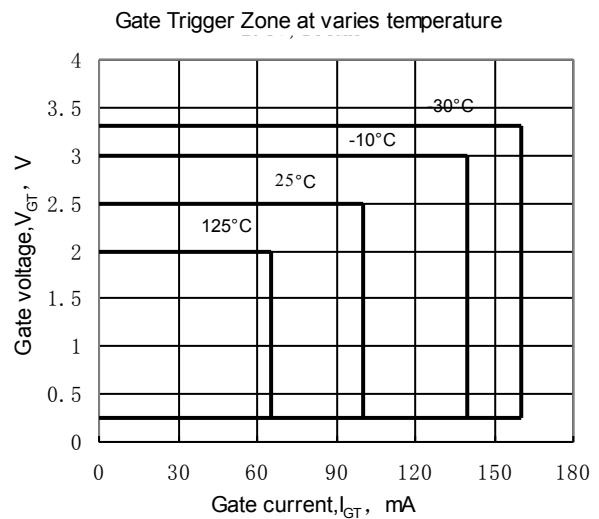
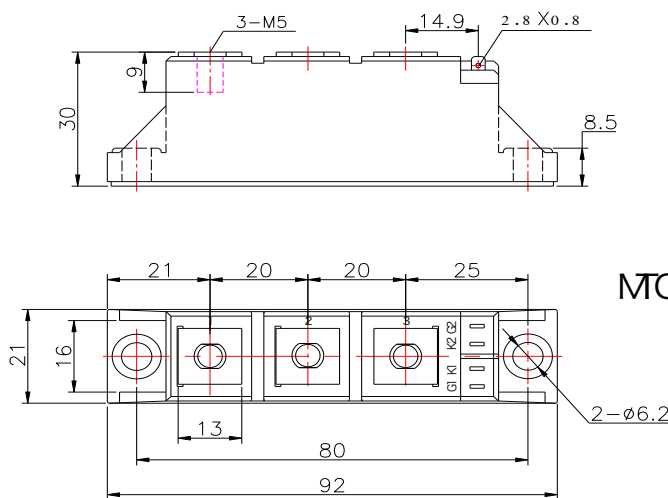


Fig.10

Outline:



MQ(B)

