**Features:**

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

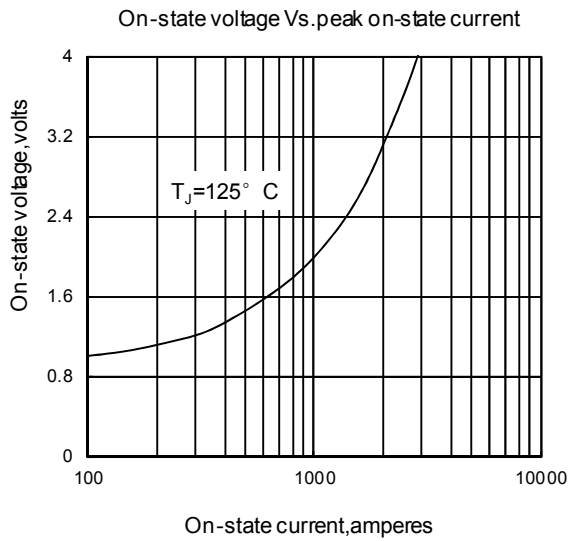
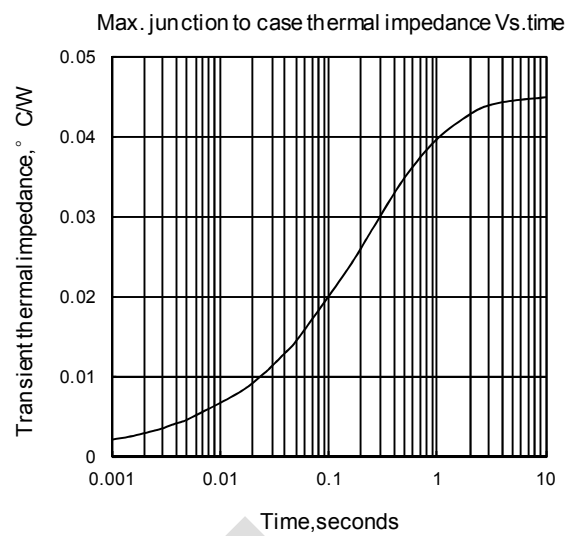
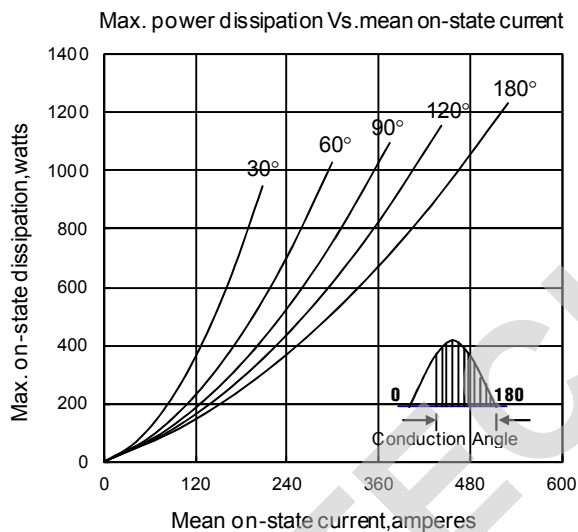
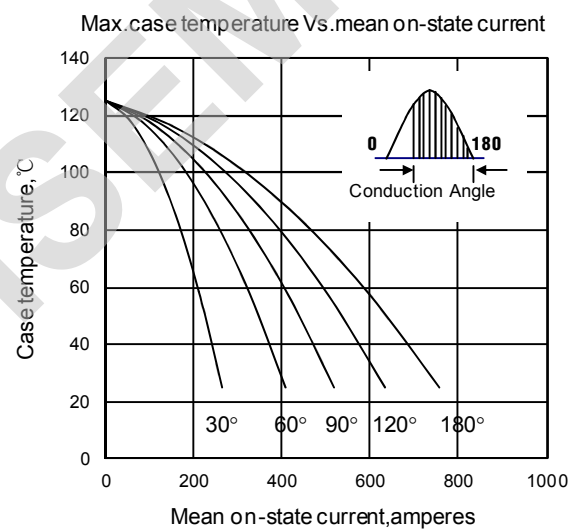
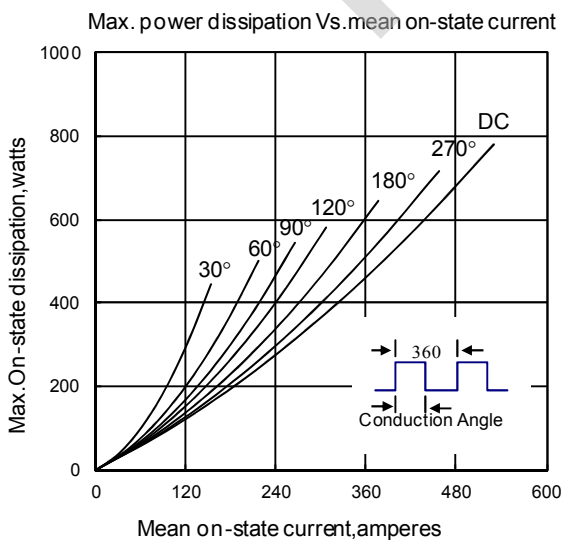
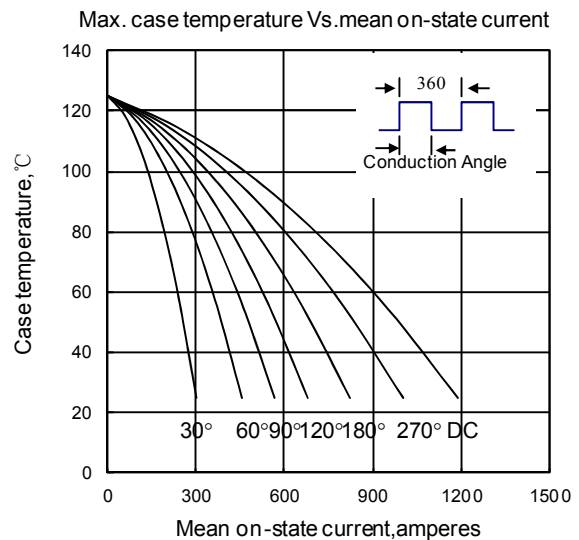
Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

Part No. Y30KPEA-KA28

$I_{T(AV)}$	530A	
V_{DRM}, V_{RRM}	1200V	1400V
	1600V	1800V

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T_j (°C)	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled, $T_c=70^\circ\text{C}$	125			530	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$t_p=10\text{ms}$	125	1100		1800	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			30	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			2.5	kA
I^2t	I^2t for fusing coordination	$V_R=0.6V_{RRM}$				31.25	$\text{A}^2\text{s}\cdot 10^3$
V_{TO}	Threshold voltage		125			0.89	V
r_T	On-state slope resistance					1.10	mW
V_{TM}	Peak on-state voltage	$I_{TM}=600\text{A}$, $F=7.0\text{kN}$	25			2.40	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			300	V/ μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$ to 800A, Gate pulse $t_r \leq 0.5\mu\text{s}$ $I_{GM}=1.5\text{A}$ Repetitive	125			100	A/ μs
Q_{rr}	Recovery charge	$I_{TM}=1000\text{A}$, $t_p=4000\mu\text{s}$, $di/dt=-20\text{A}/\mu\text{s}$, $V_R=100\text{V}$	125		600		μC
I_{GT}	Gate trigger current	$V_A=12\text{V}$, $I_A=1\text{A}$	25	35		250	mA
V_{GT}	Gate trigger voltage			0.8		2.0	V
I_H	Holding current			20		150	mA
I_L	Latching current					500	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine: double side cooled Clamping force 7.0kN				0.045	C/W
$R_{th(c-h)}$	Thermal resistance case to heatsink					0.015	
F_m	Mounting force			5.3		12	kN
T_{vj}	Junction temperature			-40		125	°C
T_{stg}	Stored temperature			-40		140	°C
W_t	Weight				85		g
Outline	KA28						

**Fig1****Fig2****Fig3****Fig4****Fig5****Fig6**

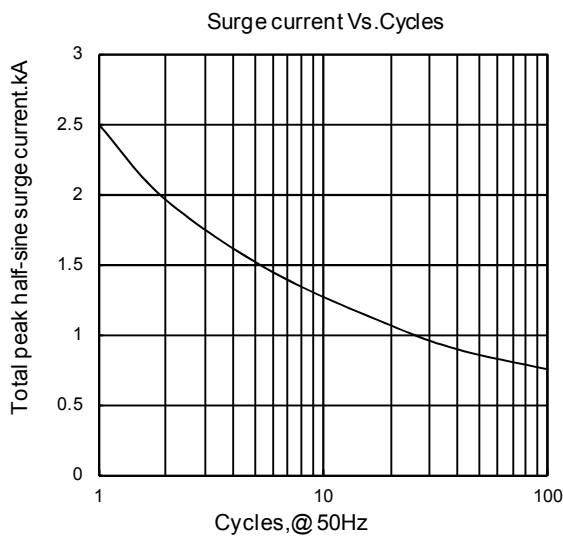


Fig7

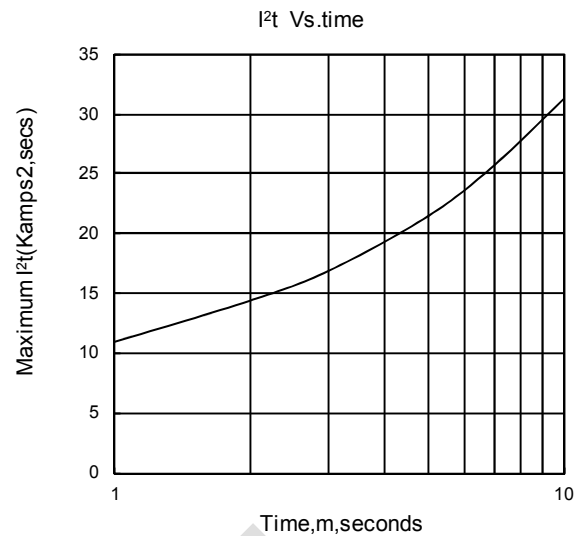


Fig8

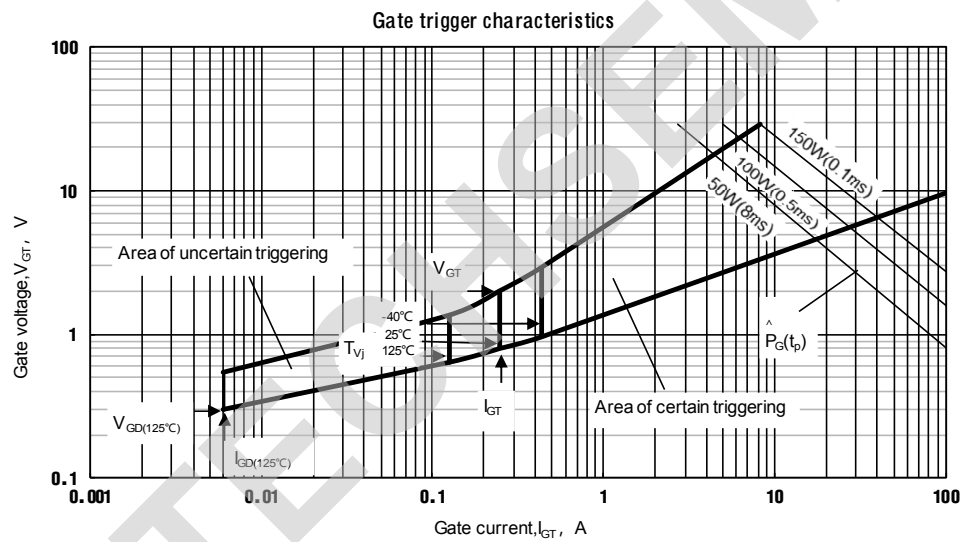


Fig.9

Outline:

